

# MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

List of Students Registered for Value Added Course

2016-17

S.No	Branch	Roll Number	Student Name	Year/ Sem	Value Added Course	DATE OF INTRODUCTI	DATE OF COMPLETIO
1	ECE	14J41A0411	C PRANAYA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
2	ECE	14J41A0413	G SHARATH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
3	ECE	14J41A0421	J AJITH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
4	ECE	14J41A0423	K SAI VIKAS	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
5	ECE	14J41A0424	KAKARLA BINDU	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
6	ECE	14J41A0425	KATI KI SAI KRISHNA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
7	ECE	14J41A0428	KONDUR ADARSH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
8	ECE	14J41A0437	METTU PRIYANKA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
9	ECE	14J41A0450	SEEMA BEGUM	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
10	ECE	14J41A0451	SHAIK YOUNIS	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
11	ECE	14J41A0452	T JYOTHI PRIYA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
12	ECE	14J41A0453	T SAI KUMAR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
13	ECE	14J41A0460	YAMINI GAJULA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
14	ECE	14J41A0465	ASAM SHRAVYA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017

*Handwritten signature*

**PRINCIPAL**  
Malla Reddy Engineering College  
(Autonomous)  
Maisammaguda, Dhulapally,  
(F... ally), Sec'bad-500 100

15	ECE	14J41A0467	CH RAJESH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
16	ECE	14J41A0468	CH SATHVIK	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
17	ECE	14J41A0474	G PUNEETH RAO	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
18	ECE	14J41A0475	GAJJELA CHITRA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
19	ECE	14J41A0476	GOLLA KEERTHI	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
20	ECE	14J41A0479	K REVANTH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
21	ECE	14J41A0480	K SAI KAMAL	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
22	ECE	14J41A0487	MADHA RENUKA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
23	ECE	14J41A0489	MEDISHETTI RAMESH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
24	ECE	14J41A0494	MOTHE SRINATH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
25	ECE	14J41A0495	MUTLURI EMMANUEL	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
26	ECE	14J41A04C1	A LIKITHA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
27	ECE	14J41A04D2	D PRASHANTH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
28	ECE	14J41A04D3	D SHIVA KRISHNA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
29	ECE	14J41A04D4	D SHIVA KUMAR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
30	ECE	14J41A04E3	K BALA KRISHNA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
31	ECE	14J41A04E4	K BHUVAN	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
32	ECE	14J41A04G6	R S PRAVALLIKA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017

*Handwritten signature*

**PRINCIPAL**  
**Malla Reddy Engineering College**  
**(Autonomous)**  
 Maisammaguda, Dhulapally,  
 (D... mpally), Sec'bad-500 100